IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Rong-Feng Chang, et al. Confirmation No. :3126
 For: TASK-BASED HARDWARE ARCHITECTURE FOR
 MAXIMIZATION OF INTELLECTUAL PROPERTY REUSE

Appl No. : 10/083,042
 Filed : February 26, 2002
 TC/A.U. : 2151
 Examiner : n/a
 Customer No. : 23380

INFORMATION DISCLOSURE STATEMENT TRANSMITTAL

Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

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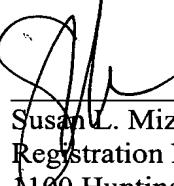
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Pursuant to Rule 37 CFR 1.97 (e)(1), each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement.

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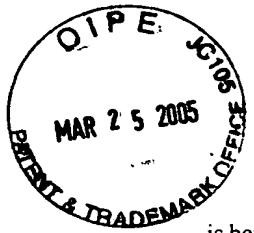
Respectfully submitted,

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Date: March 22, 2005

PATENT



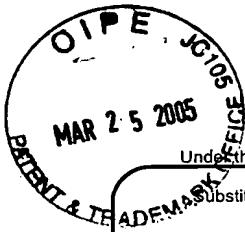
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Name: Mary Erne, Jr.

A handwritten signature in black ink, appearing to read "Mary Erne, Jr.". It is written in a cursive style with a long, sweeping line extending from the left side.



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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 2

A DEMAILED Substitute for form 1449/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		Application Number	10/083,042
		Filing Date	February 26, 2002
		First Named Inventor	Rong-Feng Chang
		Art Unit	2151
		Examiner Name	n/a
Sheet	1	of	2
		Attorney Docket Number	71795/20592

U. S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				
		EP-1 018 687-A2	12/07/2000	Lucent Technologies, Inc.		
		DE-198 27 914-C1	10/28/1999	Siemens AG		

Examiner Signature		Date Considered	
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**INFORMATION DISCLOSURE
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Sheet 2 of 2 Attorney Docket Number 71795/20592

Complete if Known

Application Number	10/083,042
Filing Date	February 26, 2002
First Named Inventor	Rong-Feng Chang
Art Unit	2151
Examiner Name	n/a

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Dave BURSKY, Hardware-Based IP Speeds Up Processor Multitasking, XP-002309804, ED Online ID #1534, February 18, 2002.	
		ZUBIN, et al., The Structure and Application of a New Multi-DSP Parallel Computing Architecture Based on ADSP-2106x,	

Examiner Signature	Date Considered
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